

<p>NOTICE OF CHANGE</p>

<p>NOT MEASUREMENT SENSITIVE</p>

MIL-STD-2217(AS)
NOTICE 2
22 May 2000

DEPARTMENT OF DEFENSE
INTERFACE STANDARD

MEMORY LOADER/VERIFIER MULTIPLEX BUS INTERFACE
WITH AVIONIC SYSTEMS, REQUIREMENTS FOR

TO ALL HOLDERS OF MIL-STD-2217(AS)

1. THE FOLLOWING PAGES OF MIL-STD-2217(AS) HAVE BEEN REVISED AND SUPERSEDE THE PAGES LISTED:

NEW PAGE	DATE	SUPERSEDED PAGE	DATE
19	22 May 2000	19	16 October 1991
20	16 October 1991	20	Reprinted without change

2. RETAIN THIS NOTICE AND INSERT BEFORE TABLE OF CONTENTS.

3. Holders of MIL-STD-2217(AS) will verify that page changes and additions indicated above have been entered. This notice page will be retained as a check sheet. This issuance, together with appended pages, is a separate publication. Each notice is to be retained by stocking points until the standard is completely revised or canceled.

Preparing activity:
Navy – AS

(Project No. GDRQ-N188)

AMSC N/A

FSC GDRQ

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

MIL-STD-2217(AS)

TABLE VII. Aircraft Type Code.

Aircraft Type	Pin 2 ⁴	Pin 2 ³	Pin 2 ²	Pin 2 ¹	Pin 2 ⁰
Not Used	0	0	0	0	0
A-4M	0	0	0	0	1
F-4S	0	0	0	1	0
F-4N	0	0	0	1	1
RF-4B	0	0	1	0	0
A-6E	0	0	1	0	1
A-6F	0	0	1	1	0
EA-6B	0	0	1	1	1
A-7E	0	1	0	0	0
AV-8B	0	1	0	0	1
AV-8C	0	1	0	1	0
F-14A+	0	1	0	1	1
F-14D	0	1	1	0	0
F/A-18A/B/C/D	0	1	1	0	1
Spare	0	1	1	1	0
V-22	0	1	1	1	1
F-14A	1	0	0	0	0
AH-1Z	1	0	0	0	1
UH-1Y	1	0	0	1	0
Spare Codes	1	0	0	1	1
Through	1	1	1	1	0
Reserved	1	1	1	1	1

Notes: (1) 0 = Open to Ground
1 = Short to Ground

(2) Since the 0-0-0-0-0 combination is never used, a 0-0-0-0-0 code read by the MLV would indicate that the cable is not attached.

SUPERSEDES PAGE 19 OF MIL-STD-2217(AS).

MIL-STD-2217(AS)

5.1.2.4.5.1 AN/AYK-14 Discrete Usage. This standard allows for up to four Avionics Buses with an IPL, IPL Fail and PWR OFF/ON discrete assigned to each bus. For the special case where a second AN/AYK-14 (acting as an RT) is on a bus, the IPL, IPL Fail and PWR OFF/ON discrete from an unused bus can be utilized to control the second AN/AYK-14.

5.1.2.4.6 Boot Enable Stores Management System (SMS). The Boot Enable SMS signal is utilized for aircraft with an SMS requiring this input to enable the boot strap load operation. The interface is a contact closure internal to the MLV with a maximum current of 200 ma. and a maximum open circuit voltage of 32 VDC and is applicable to Appendix F operation.

5.1.2.4.7 AN/ALQ-165 External Processor ON/OFF Discrete. A one wire discrete to control power on/off of the ALQ-165 during reprogramming is provided. This discrete shall be utilized by the MLV when use is indicated in the Lookup Table (see 6.1.c.3). The purpose of this discrete is to limit ALQ-165 power on time to less than 1 minute for a Load/Verify of 32K data words of UDM. This discrete is shorted to the Reference Ground signal (200 ma. maximum current, 32 VDC maximum open circuit voltage) associated with the EW UDM and OSM Reprogram Enable discrettes by the MLV 500 msec prior to any multiplex bus communications with AN/ALQ-165. The MLV shall open this line (open circuit impedance to Reference Ground $>$ or $=$ 100K ohms) after the completion of a Load/Verify or a Verify (indicated by Busy Bit clear in STATUS RESPONSE WORD of the Activity Message after the Reprogram Control Message at the end of the Verify (see Appendix B)). Additionally the MLV will open this line 10 seconds after halting to display an error requiring operator input or after the total on time programmed in the Lookup Table (see 6.1.1) (nominally 3 minutes).

5.1.2.4.8 MLV Reserved Pins. Four pins of the interface connector are to be open circuit in the aircraft wiring in order to allow for MLV self test functions through the interface connector.

5.1.2.4.9 F/A-18 Reserved Pins (Type I only). Five pins are reserved on the Type I interface connector for utilization of existing F/A-18 functions. The reserved pins are: Maintenance Signal Data Recording Set (MSDRS) OFF/ON two wire contact closure 200 ma. maximum current, 32 VDC maximum open circuit voltage; BOOT ENABLE Radar Data Processor (RDP) two wire contact closure 200 ma. maximum current, 32 VDC maximum open circuit voltage; and, a one wire Relay Test signal maximum 32 VDC signal at 200 ma.

5.1.2.4.10 Associated Ground Pins and Spares. Pins for shield/grounding and designated spares are provided as called out in Tables II and IV.

5.1.2.5 Mechanical Characteristics. The aircraft signal cable connectors will be mounted on a surface of the aircraft accessible to O-Level personnel. A metal protective cap shall be provided to cover the connector and shall be physically attached to the aircraft.

5.1.2.6 F/A-18 Bus and Discrete Signal Connections. The F/A-18A/B/C/D aircraft utilizes an existing 37 pin connector for bus and discrete signal connections that does not comply with this standard. However, all MLV

REPRINTED WITHOUT CHANGE