

## DATA ITEM DESCRIPTION

**Title:** Integrated Circuit Graphics Database

**Number:** DI- IPSC-80409A

**AMSC Number:** 7644

**DTIC Applicable:** No

**Office of Primary Responsibility:** NS/I5221

**Applicable Forms:** N/A

**Approval Date:** 21 NOV 2006

**Limitation:** N/A

**GIDEP Applicable:** No

### Use/relationship:

This data base documents all geometric and associated layout information used in the generation on integrated circuits.

This Data Item Description (DID) contains the format and content preparation instructions for the integrated circuit graphics data base generated by the specific and discrete task requirement as delineated in the contract.

Normally used when custom integrated circuits are being developed in conjunction with other integrated circuit data items.

This DID supercedes DI-IPSC-80409.

### Requirements:

1. Reference documents. None.

2. Format and Content:

2.1 The contractor shall prepare and deliver cell development data in accordance with the following:

- a) The contractor shall utilize and submit the computer aided cell development data on digital media in the following order of preference:
  - i. CD-ROM
  - ii. 8mm Helical Scan Tape
  - iii. 4mm Digital Audio Tape
- b) The contractor shall utilize electronic media, in accordance with the instructions for digital submissions, for all other documentation and reports.

2.2. Database file format:

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- a. GDS II Files shall be in accordance with the GDS II stream format. Note: Data shall be in uncompressed format.
- b. Information contained in the library shall be complete. Any graphic cell (or structure) used in describing the integrated circuit or any other graphic cell (including test) shall be included in the library.

### 2.3. Documentation.

2.3.1. The following information shall be provided on 8 ½ x 11 inch (Metric size A4) paper:

- a. Title. Chip name/project name.
- b. Company/division.
- c. Contract number.
- d. Revision date of chip or cells (as appropriate).
- e. Circuit description.
- f. GDS II stream format.
- g. Name(s) of library or libraries.
- h. Name of graphic cell or structure containing the chip description.
- i. Cross reference of graphic layers as they correspond to fabrication.  
**Note: For clarification, this is the “Layer Map Table”.**
- j. Listing of the line code table used to resolve polygons (if applicable).
- k. Data scale in design units (i.e., Mils, Microns, etc) per GDS.
- l. Date created.
- m. Revision date of chip or cells (as appropriate).
- n. Chip 0N number.
- o. Classification.
- p. Number of files.
- q. Contract number.

2.3.2. A peel-off label shall be affixed to each media with the following information:

- a. Chip name/project name.
- b. Date created.
- c. GDS II stream format
- d. Name(s) of library or libraries.
- e. Classification.

3. END OF DI-IPSC-80409A